

AMENDMENTS TO THE CLAIMS

1. (currently amended) An apparatus for performing a pilot synchronization operation in a wireless communication system, the apparatus comprising:
a plurality of sliding correlators that each receives a portion of a received correlation sequence and provides a partial correlation output;
~~an a plurality of absolute value blocks that takes the a respective~~
absolute value of each partial correlation output; and
circuitry that combines the absolute values of each of the ~~partial correlation-absolute value~~ partial correlation outputs to form a correlation output.
2. The apparatus set forth in claim 1, wherein each of the plurality of sliding correlators receives a portion of a stored correlation sequence for comparison to the portion of the received correlation sequence.
3. The apparatus set forth in claim 1, wherein the correlation output comprises a correlation peak.
4. (currently amended) The apparatus set forth in claim 3, wherein the correlation peak corresponds to a ~~P~~primary synchronization channel (SCH) channel.
5. (currently amended) The apparatus set forth in claim 3, wherein the correlation peak corresponds to a ~~S~~secondary synchronization channel (SCH) channel.
6. The apparatus set forth in claim 1, wherein the apparatus comprises a portion of a code division multiple access receiver.
7. The apparatus set forth in claim 1, wherein the apparatus comprises a portion of a receiver that complies with the Universal Mobile Telecommunications System ("UMTS") Wideband Code Division Multiple Access ("WCDMA") standard.
8. The apparatus set forth in claim 1, wherein the apparatus comprises at least a portion of a cell search block.

9. (currently amended) A code division multiple access ("CDMA") receiver, comprising:

an analog-to-digital converter that receives a CDMA signal and converts the CDMA signal into a digital signal;

a matched filter that filters the digital signal to produce a filtered digital signal;

a tapped delay line that receives the filtered digital signal and produces a delayed filtered digital signal; and

a cell search block, comprising:

a plurality of sliding correlators that each receives at least a portion of the delayed filtered digital signal and provides a partial correlation output;

an absolute value block that takes the absolute value of each partial correlation output; and

circuitry that combines the absolute values of the ~~partial correlation outputs~~ absolute value block to form a correlation output.

10. The CDMA receiver set forth in claim 9, wherein each of the plurality of sliding correlators receives a portion of a stored correlation sequence for comparison to the portion of the received correlation sequence.

11. The CDMA receiver set forth in claim 9, wherein the correlation output comprises a correlation peak.

12. (currently amended) The CDMA receiver set forth in claim 11, wherein the correlation peak corresponds to a ~~P~~primary synchronization channel (SCH) channel.

13. (currently amended) The CDMA receiver set forth in claim 11, wherein the correlation peak corresponds to a ~~S~~secondary synchronization channel (SCH) channel.

14. The CDMA receiver set forth in claim 9, wherein the apparatus comprises a portion of a code division multiple access receiver.

15. The CDMA receiver set forth in claim 9, wherein the CDMA receiver complies with the Universal Mobile Telecommunications System ("UMTS") Wideband Code Division Multiple Access ("WCDMA") standard.
16. A method for forming a correlation output in a wireless communication system, the method comprising:
- receiving a correlation sequence to produce a received correlation sequence;
 - segmenting the received correlation sequence into a plurality of partial correlation sequences;
 - comparing each partial correlation sequence to a portion of a stored correlation sequence;
 - producing a partial correlation output based on the comparison of each partial correlation sequence to the corresponding stored correlation sequence;
 - determining the absolute value of each partial correlation output; and
 - combining the absolute values of each of the partial correlation outputs to form a correlation output.
17. The method set forth in claim 16, comprising identifying a correlation peak in the correlation output.
18. (currently amended) The method set forth in claim 17, comprising identifying a ~~P~~primary synchronization channel (SCH) channel based on the correlation peak.
19. (currently amended) The method set forth in claim 17, comprising identifying a ~~S~~secondary synchronization channel (SCH) channel based on the correlation peak.
20. The method set forth in claim 16, wherein the recited acts are performed in the recited order.